Reviewed by: Eric Wagner

Module Name: rtc\_stopwatch

Jira Ticket: SWATCH2-22

**Stopwatch Requirements Checklist Rev. 1**

1. Is requirements document written clearly and grammatically correct?

Yes [ ] No [X]

Comments: Missing period at the end of requirement bodies.

1. Does requirements document define all tasks to be performed?

Yes [ ] No []

Comments: For INIT\_01 describe top level reset in terms of I/O.

1. Does requirements document include all inputs and outputs to the module along with their corresponding accuracy, range of values, frequency and format?

Yes [ ] No [X]

Comments: Use pinout to describe sys\_clk for CLK\_01 and trigger\_in for IN\_01.

1. Is each requirement unique?

Yes [X] No [ ]

Comments:

1. Does requirements document comply with system requirements and traceability?

Yes [ ] No [X]

Comments: Naming convention is not inline with System Requirements (ie use reset\_n and trigger\_n instead of rst\_n and ‘input to Trigger Mod’).

1. Is terminology consistent with standards?

Yes [X] No [ ]

Comments:

1. Is it possible to implement all requirements?

Yes [X] No [ ]

Comments:

1. Is each requirement testable or verifiable?

Yes [X] No [ ]

Comments: